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October 24, 2003

To: Commissioner for Patents P.O.Box 1450 Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572 28 Davis Avenue Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/631,841 07/31/03

Chun Shiah

LOW JITTER INPUT BUFFER WITH SMALL INPUT SIGNAL SWING

Grp. Art Unit:

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation In An Application.

The following Patents and/or Publications are submitted to comply with the duty of disclosure under CFR 1.97-1.99 and 37 CFR 1.56. Copies of each document is included herewith.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 7, 2003.

Stephen B. Ackerman, Req.# 37761

Signature/Date

10/27/03

ETRON-01-010

- U.S. Patent 5,978,310 to Bae et al., "Input Buffer for a Semiconductor Memory Device, " describes an input buffer for a DRAM memory device, which removes noise from the row address strobe.
- U.S. Patent 6,002,618 to Komarek et al., "NMOS Input Receiver Circuit," discloses an NMOS input receiver circuit for a read only memory.

Sincerely

Stephen B. Ackerman,

Reg. No. 37761

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citation if not in conformance and not considered. Include copy of this form with next communication to the applicant